Title: Semiconductor Integrated Circuit Having Reduced Cross-Talk Noise

Inventor: K. Uyshiyama et al. Application No. New Division Docket No. 100353-00176

FIG. 1A

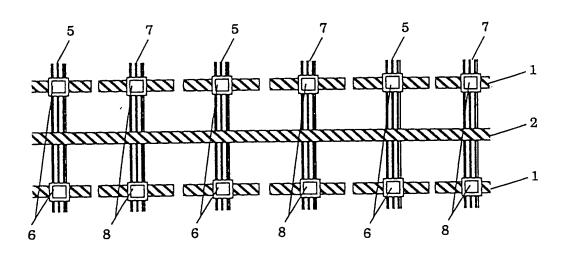
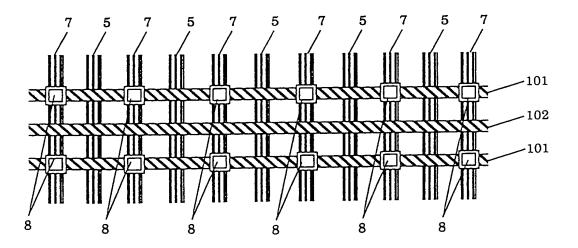


FIG. 1B



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FIG. 2

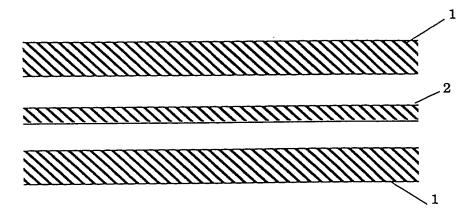
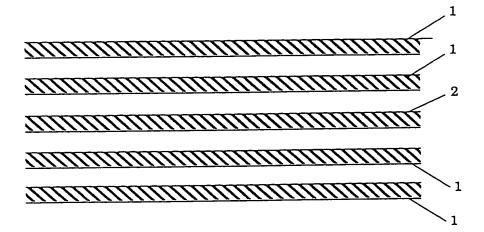


FIG. 3



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FIG. 4

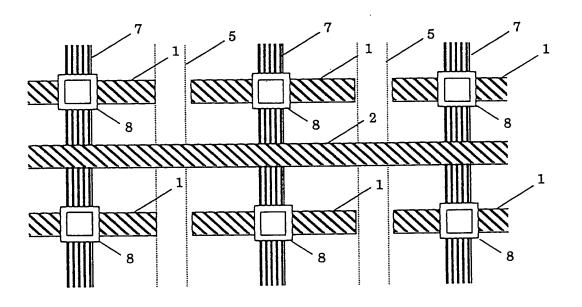
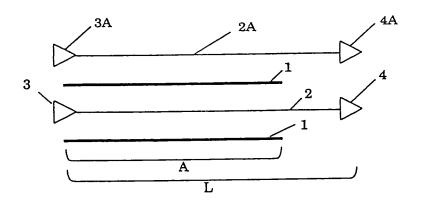


FIG. 5



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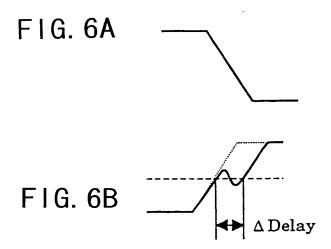


FIG. 6C

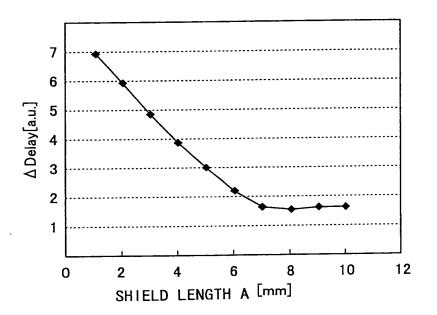
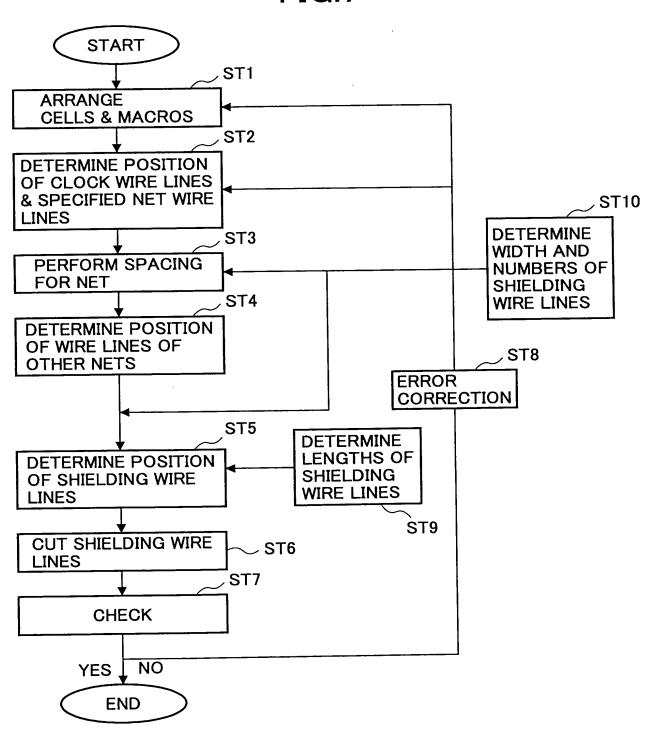
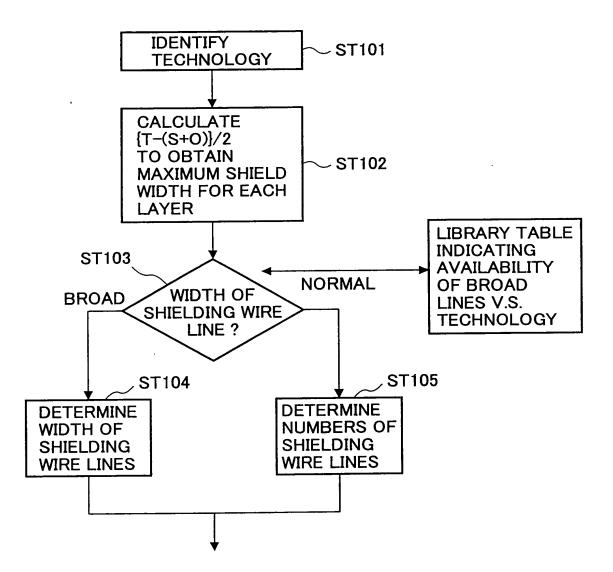


FIG.7



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FIG.8



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FIG.9

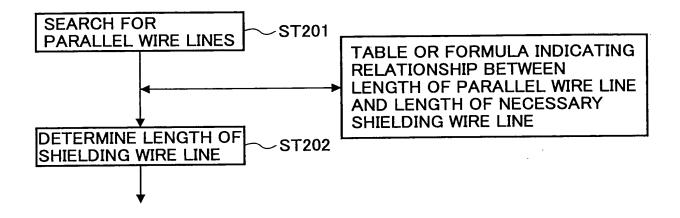


FIG.10

		NECESSARY SHIELDING WIRE LINE						
LENGTH OF PARALLEL WIRE LINE		10	9	8	7	6	5	4
	10	85%	85%	85%	85%	75%	65%	
	9	•	•	•	•	•		_
	8	•	•	•	•	•		
	•	•	•					
	•							